

FIG. 1 PRIOR ART

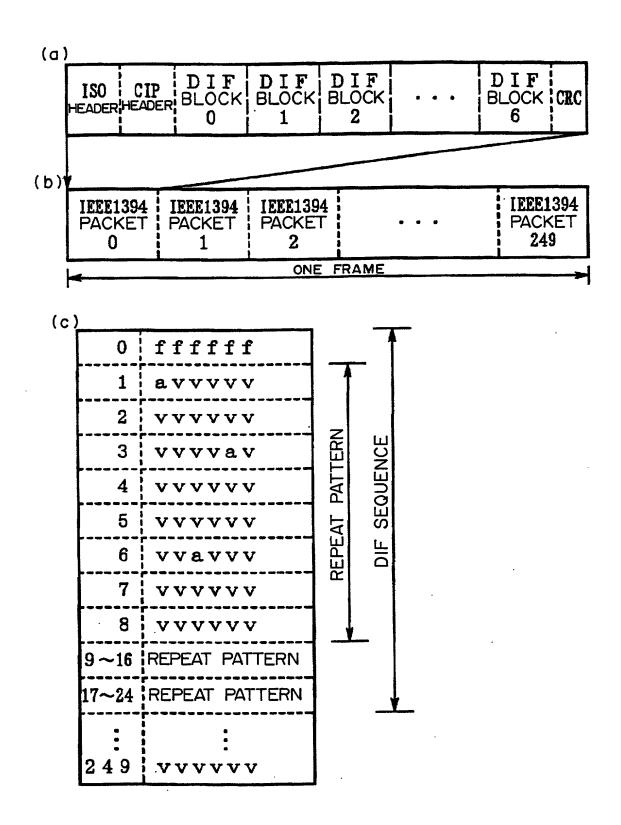


FIG. 2 PRIOR ART

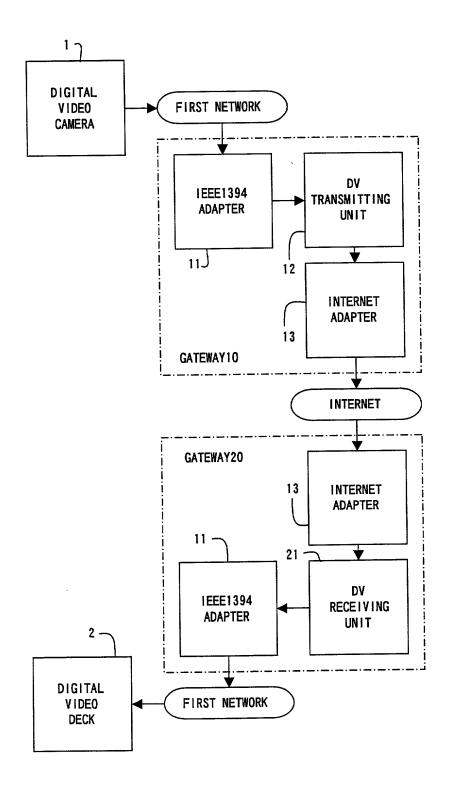


FIG. 3

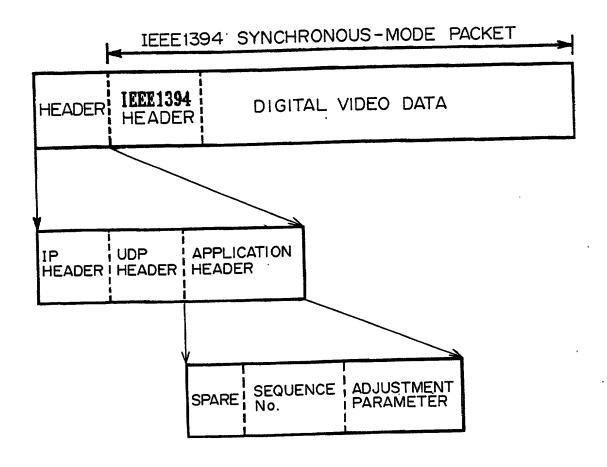


FIG. 4 PRIOR ART

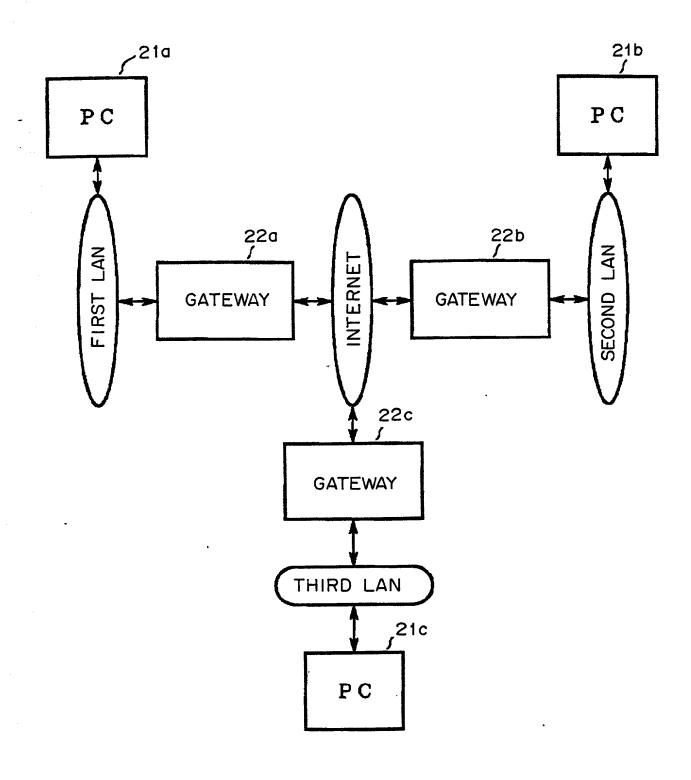
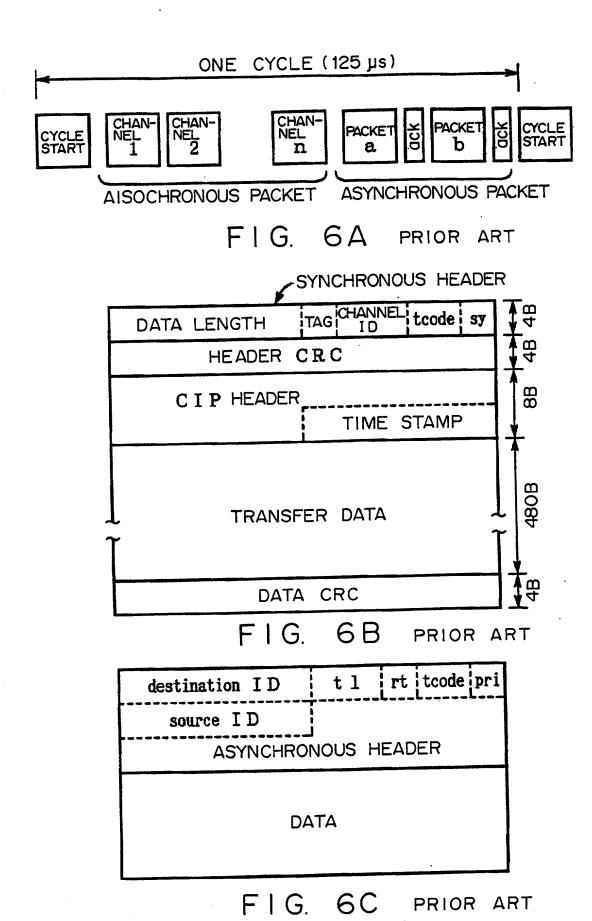
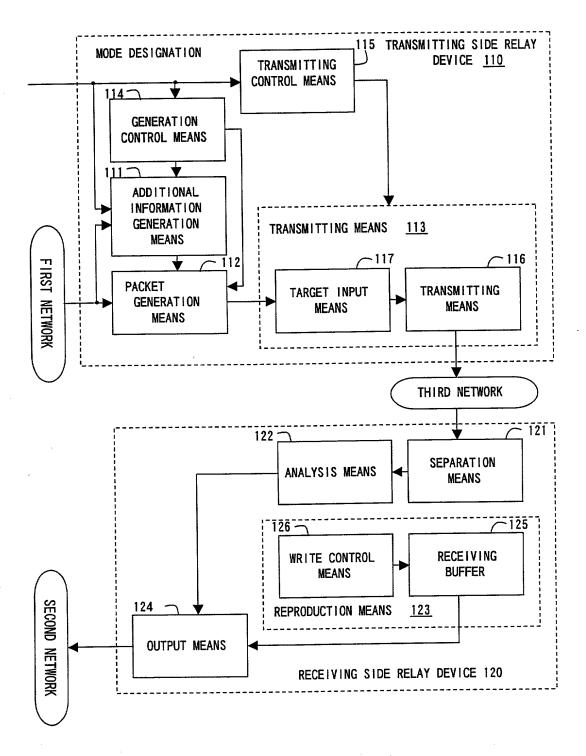
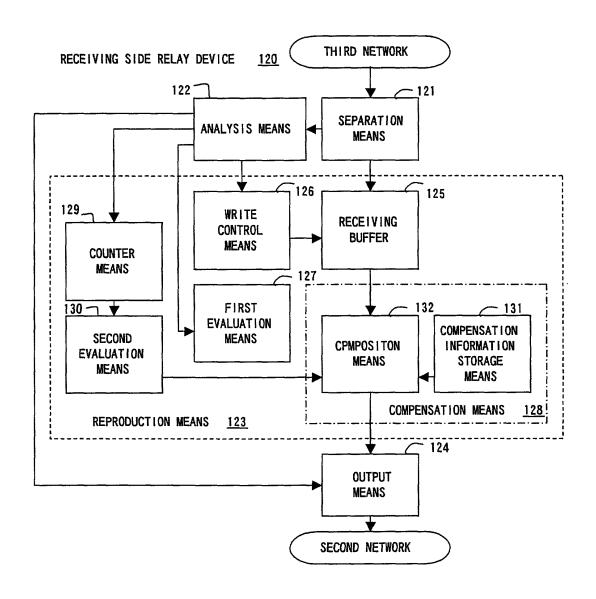


FIG. 5 PRIOR ART

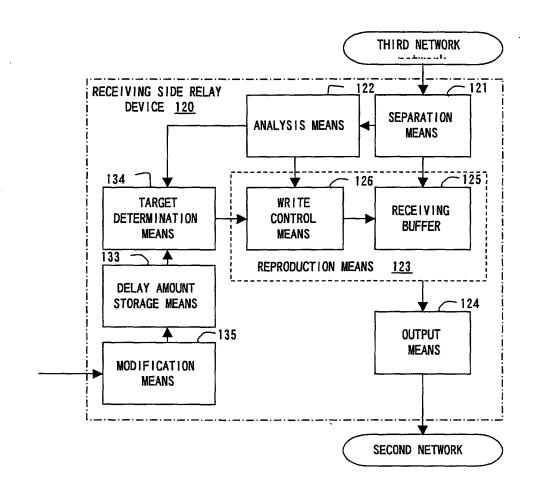




F I G. 7



F | G. 8



F I G. 9

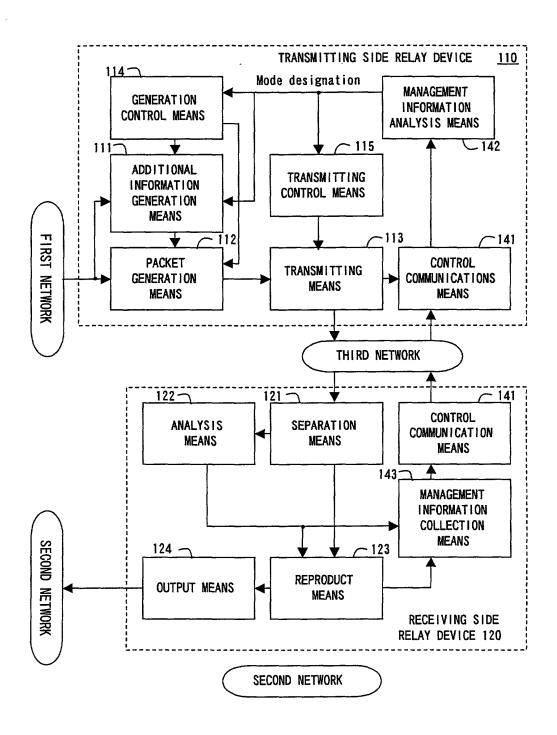


FIG. 10

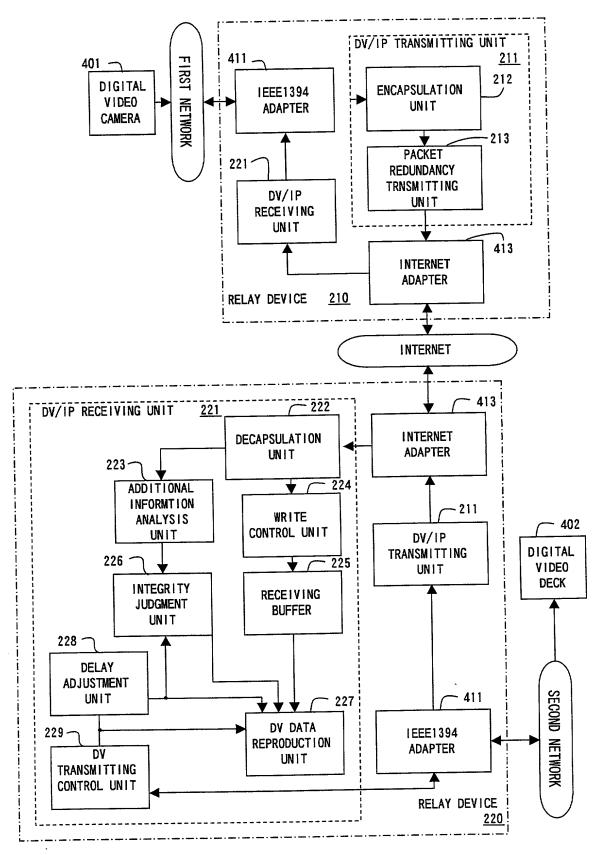


FIG. 11

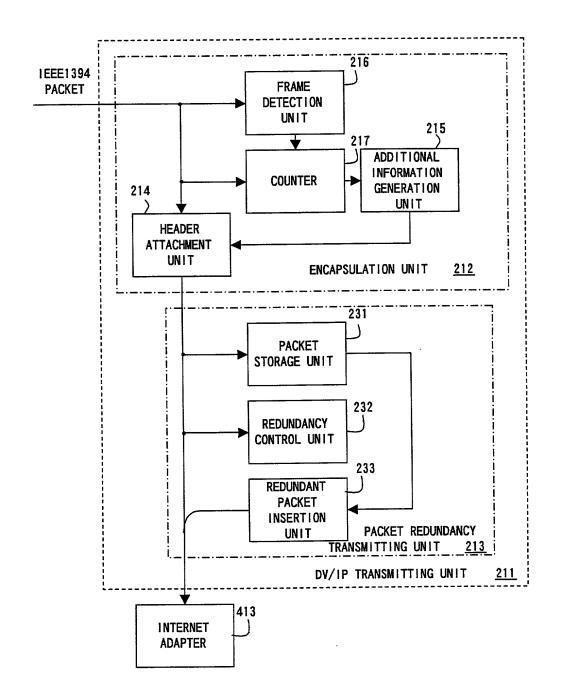


FIG. 12

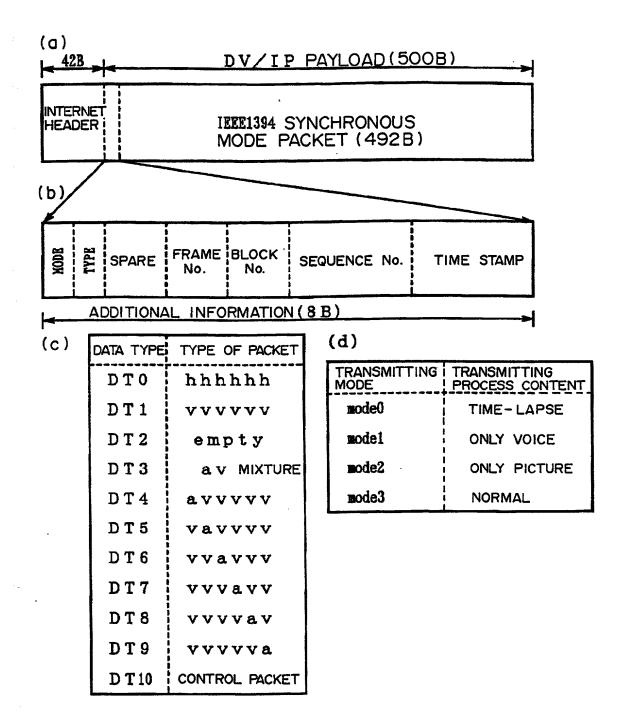


FIG. 13

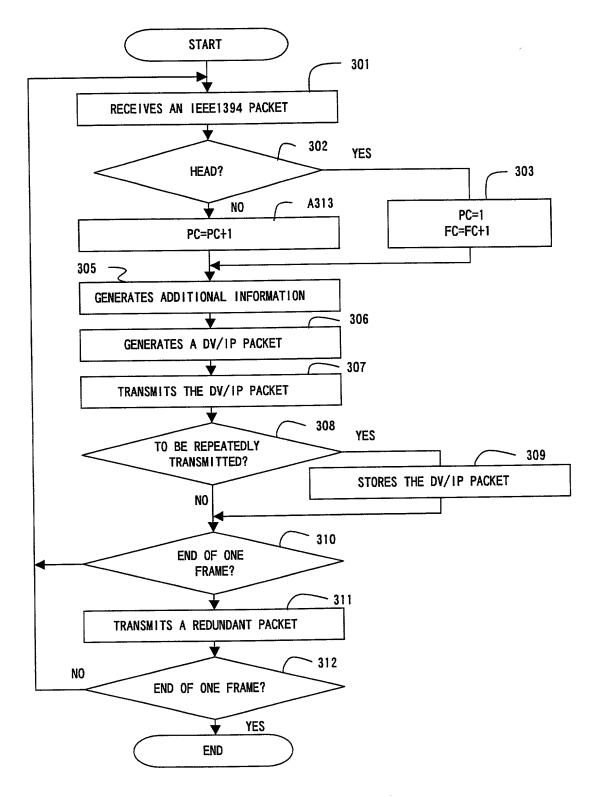
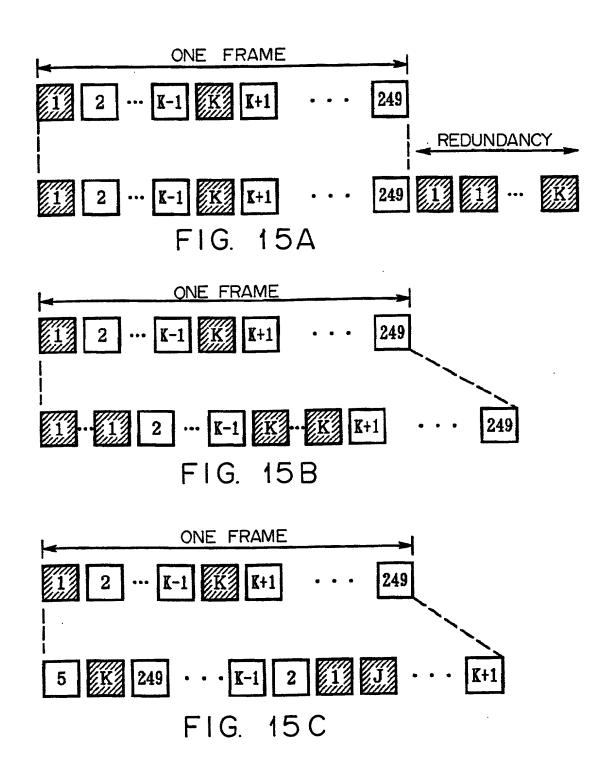


FIG. 14



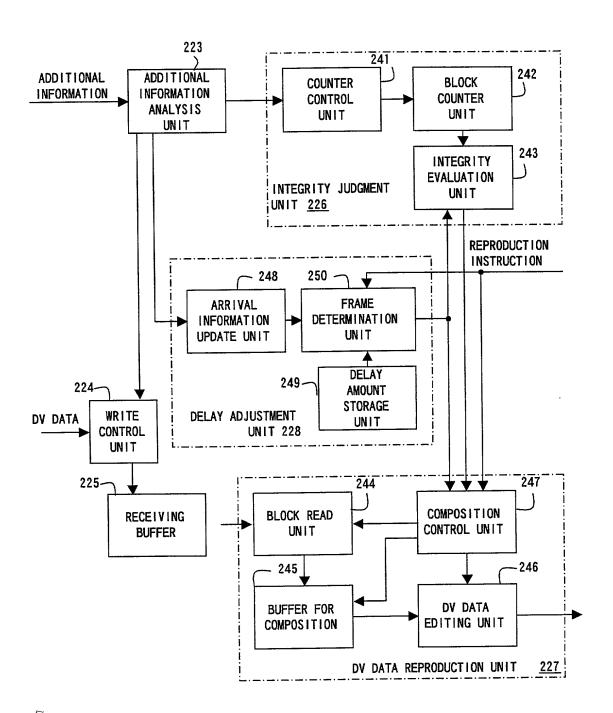


FIG. 16

BLOCK No.	FRAME(0)	FRAME(1)		FRAME(K)
0	DV 0, 0	DV 1, 0		DV _K , o
1	DV _{0,1}	DV 1, 1		DV _{K, 1}
2	DV 0, 2	DV 1, 2		DV _K , 2
	1 1 2			
	•			
•		•	•	
2 4 9	DV ₀ , 249	DV ₁ , 249		DVK, 249

FIG. 17A

	FRAME(0)	FRAME(1)	• • •	FRAME(K)
RECEIVING BLOCK	RBC0	RBC1		RBCK
FRAME HEADER	FHC0	FHC1		FHCK
AUDIO BLOCK	ABC0	ABC1		ABCK
VIDEO BLOCK	VBC0	VBC1		VBCK

FIG. 17B

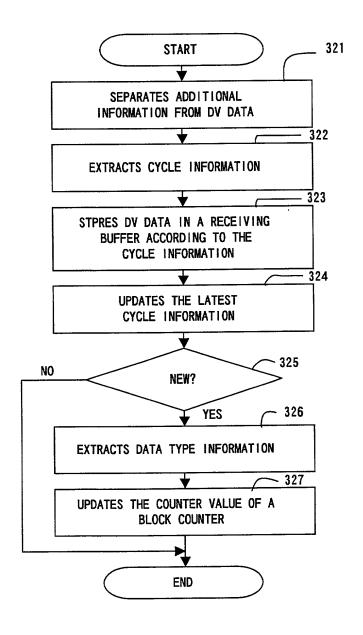


FIG. 18A

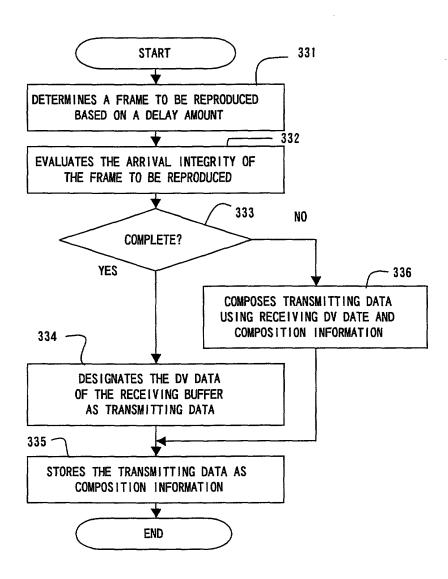


FIG. 18B

FIRST BUFFER	SECOND BUFFER	PACKET No.	TRANSMITTING DATA
DV _(n-1, 0)	DV _(n, 0)	0	DV _(n, 0)
DV _(n-1, 1)	DV (n, 1)	1	DV _(n, 1)
DV _(n-1, 2)	LOST	2	DV (n-1, 2)
:	:	:	
DV _(n-1, 249)	DV (n, 249)	249	DV _(n, 249)

FIG. 19A

PACKET No.	FIRST BUFFER	SECOND BUFFER
0	FH _{n-1}	FH _n
1	$\mathbf{a}_{\scriptscriptstyle n-1}\mathbf{v}_{\scriptscriptstyle n-1}\mathbf{v}_{\scriptscriptstyle n-1}\mathbf{v}_{\scriptscriptstyle n-1}\mathbf{v}_{\scriptscriptstyle n-1}\mathbf{v}_{\scriptscriptstyle n-1}$	a v v v v v v v v v v v v v v v v v v v
2	$\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}$	LOST
3	$\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{e}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}$	v _n v _n v _n a _n v _n
:	ŧ	:
249	$\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}$	LOST

FIG. 19B

PACKET No.	TRANSMITTING DATA		
0	FH _{n-1}		
1	$\mathbf{a}_{n} \mathbf{v}_{n-1} \mathbf{v}_{n-1} \mathbf{v}_{n-1} \mathbf{v}_{n-1} \mathbf{v}_{n-1}$		
2	$\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}$		
3	$\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{v}_{\mathtt{n-1}}\mathbf{a}_{\mathtt{n}}\mathbf{v}_{\mathtt{n-1}}$		
:			
249	$\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}$		

FIG. 19C

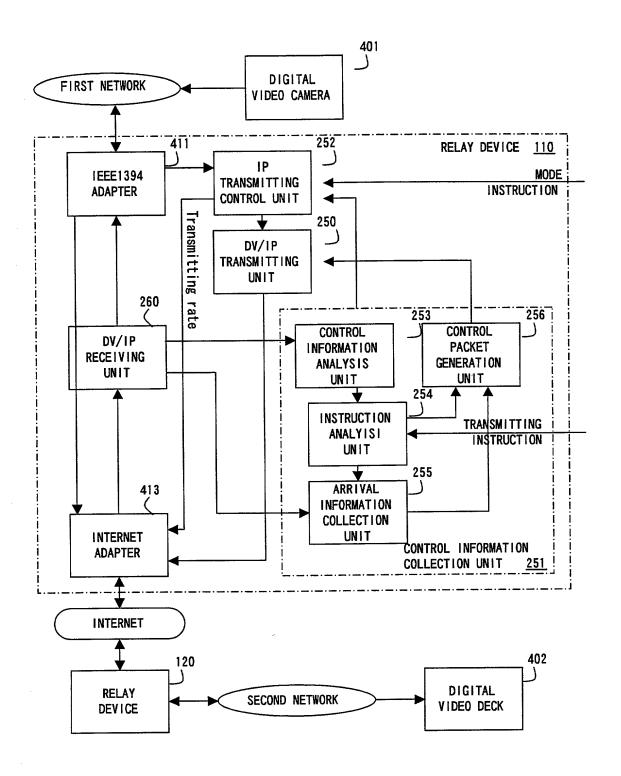


FIG. 20

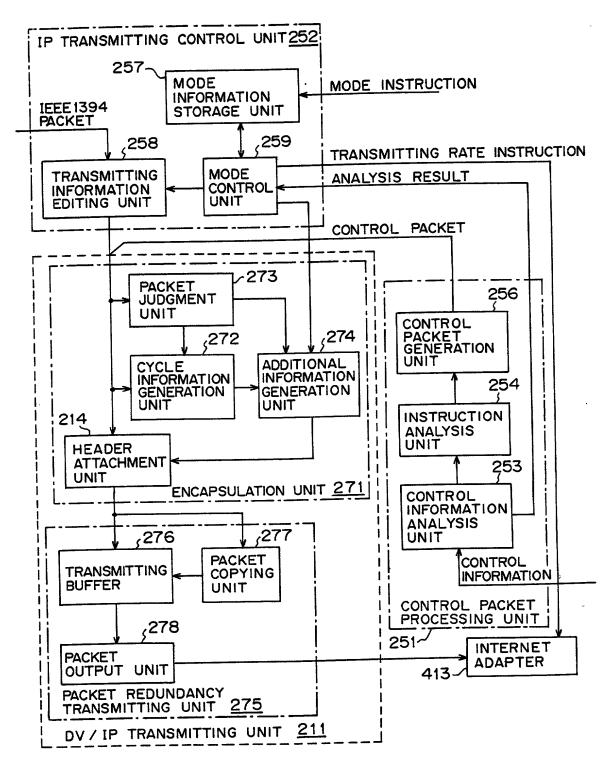


FIG. 21

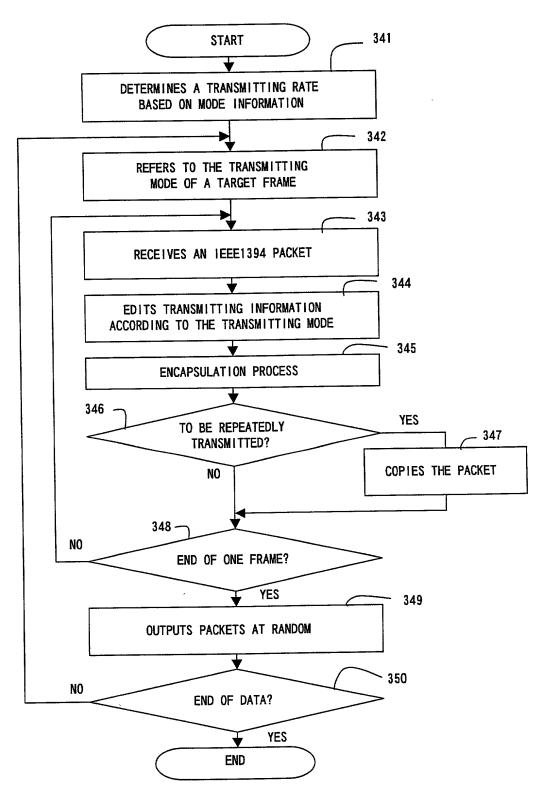


FIG. 22

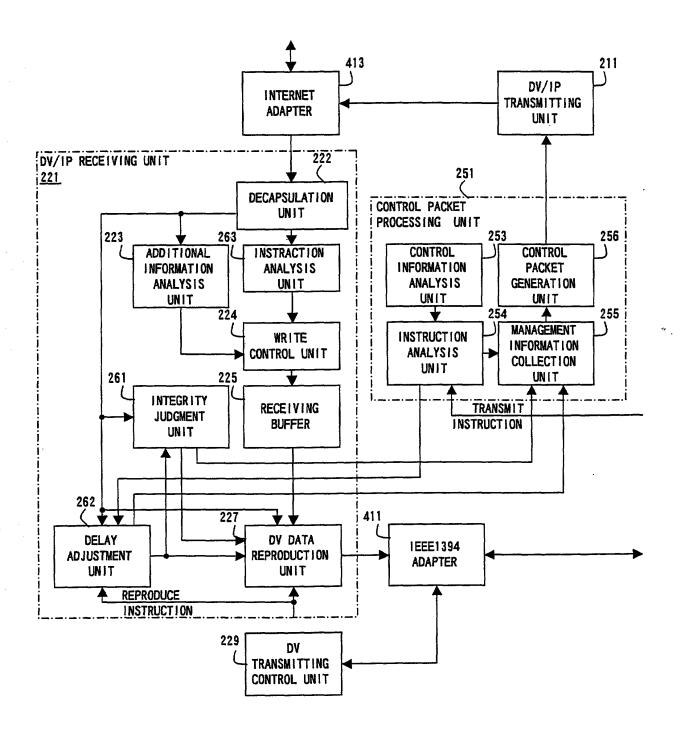


FIG. 23

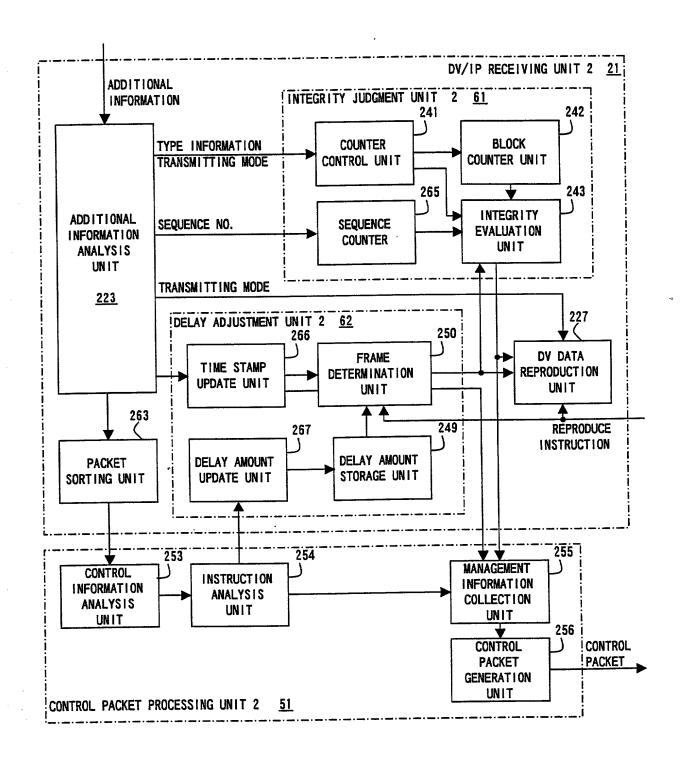


FIG. 24

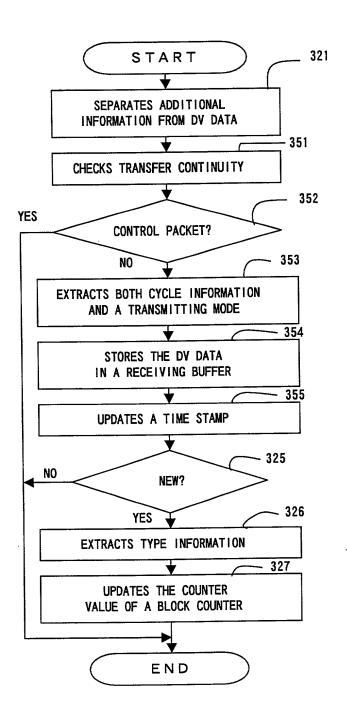


FIG. 25A

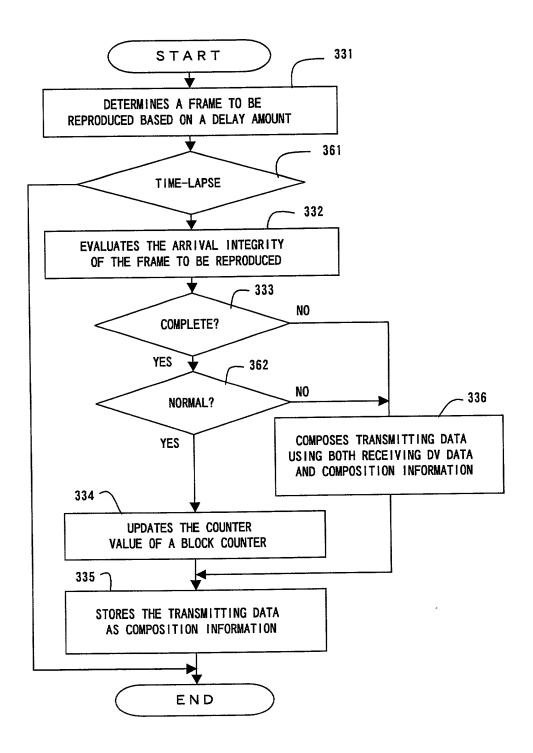


FIG. 25B

PACKET No.	ONLY VOICE)) [ONL	ΥP	ICTU	JRE		
0	FH					!	7775	F	H			
1	a	v	v	v	v	v	-	v	v	v	v	v
2	-		-	_	-	. -	v	v	v	v	v	v
3	v	v	v	v	a	v	v	v	v	v		v
					} }			:				
249		_			· — .		V	V	V	v	v	v

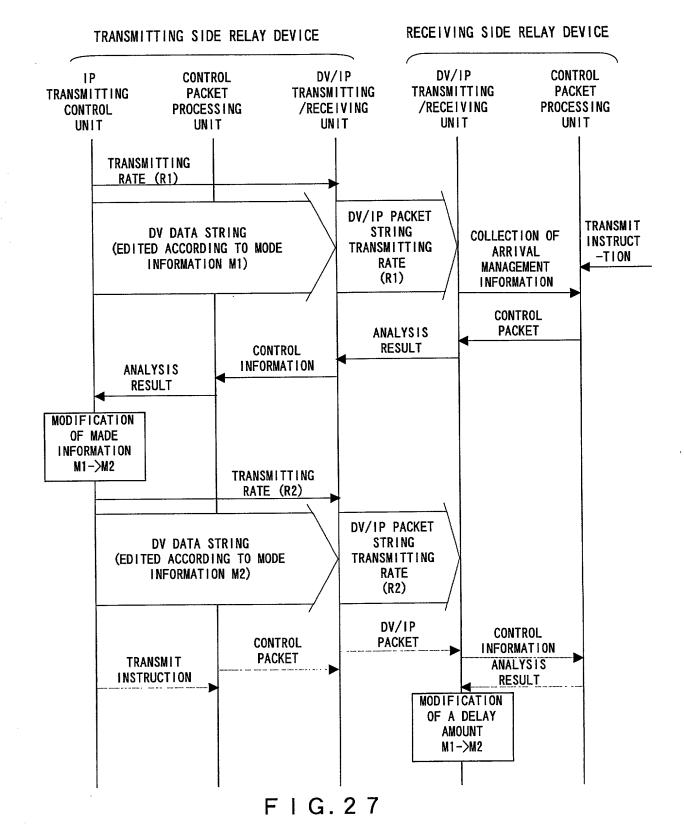
FIG. 26A

PACKET No.	FIRST BUFFER	SECOND BUFFER
0	FH _{n-i}	FH,
1	$\mathbf{a}_{\scriptscriptstyle n-1} \mathbf{v}_{\scriptscriptstyle n-1} \mathbf{v}_{\scriptscriptstyle n-1} \mathbf{v}_{\scriptscriptstyle n-1} \mathbf{v}_{\scriptscriptstyle n-1} \mathbf{v}_{\scriptscriptstyle n-1}$	a
2	$\mathbf{v}_{a-1}\mathbf{v}_{a-1}\mathbf{v}_{a-1}\mathbf{v}_{a-1}\mathbf{v}_{a-1}\mathbf{v}_{a-1}$	
3	$\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{a}_{n-1}\mathbf{v}_{n-1}$	a _a -
	:	:
249	$\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}$	

FIG. 26B

PACKET No.	TRANSMITTING DATA
0	FH _{n-1}
1	$\mathbf{a}_{n} \mathbf{v}_{n-1} \mathbf{v}_{n-1} \mathbf{v}_{n-1} \mathbf{v}_{n-1} \mathbf{v}_{n-1}$
2	$\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{v}_{n-1}\mathbf{v}_{n-1}$
3	$v_{n-1}v_{n-1}v_{n-1}v_{n-1}a_nv_{n-1}$
:	
249	$\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}\mathbf{V}_{n-1}$

FIG. 26C



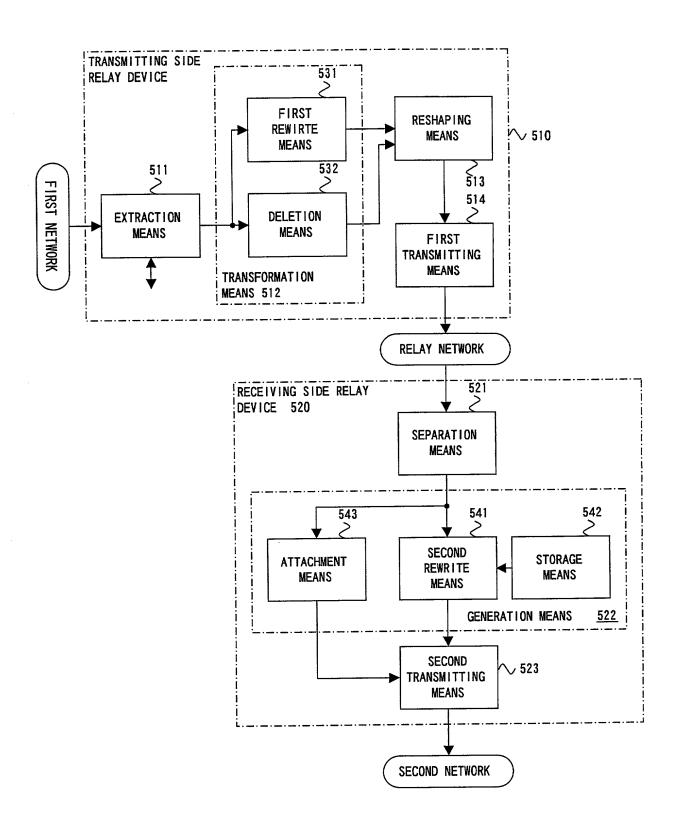


FIG. 28

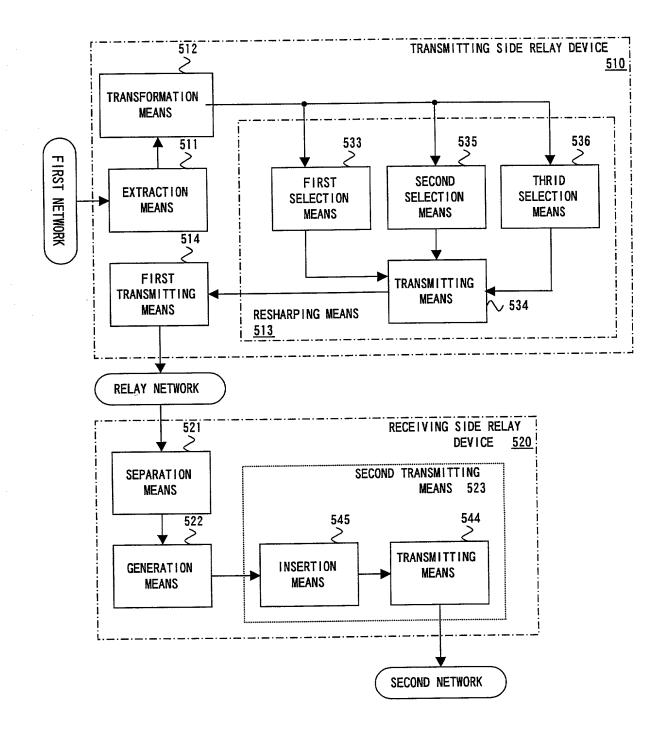


FIG. 29

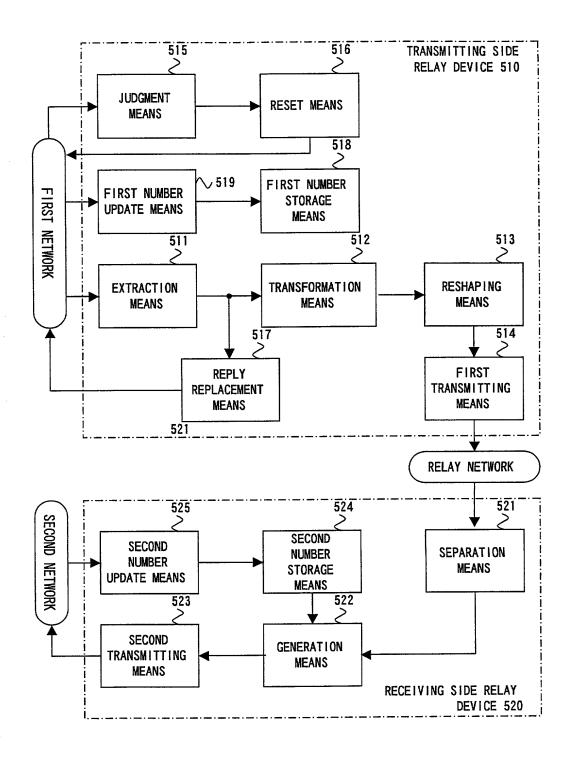


FIG. 30

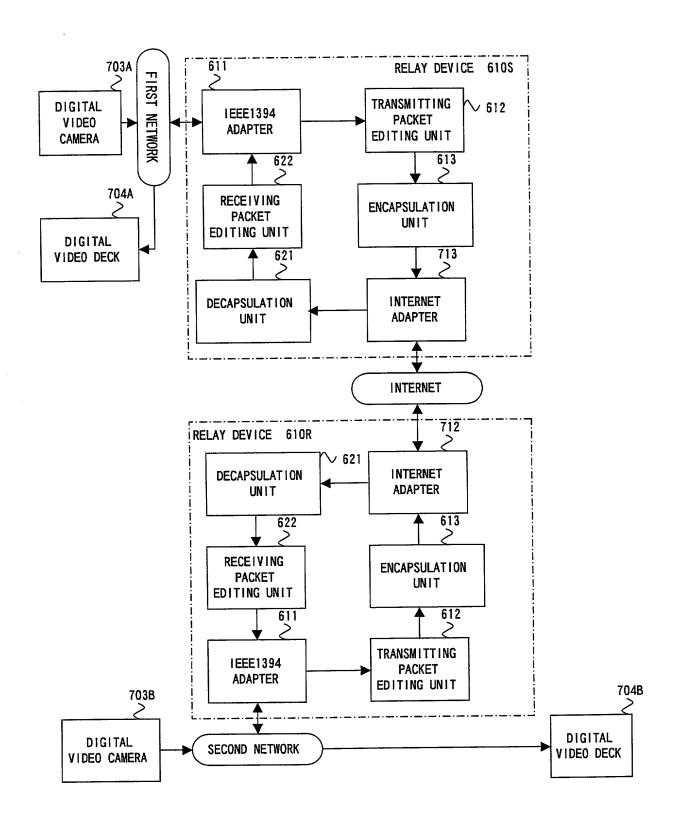


FIG. 31

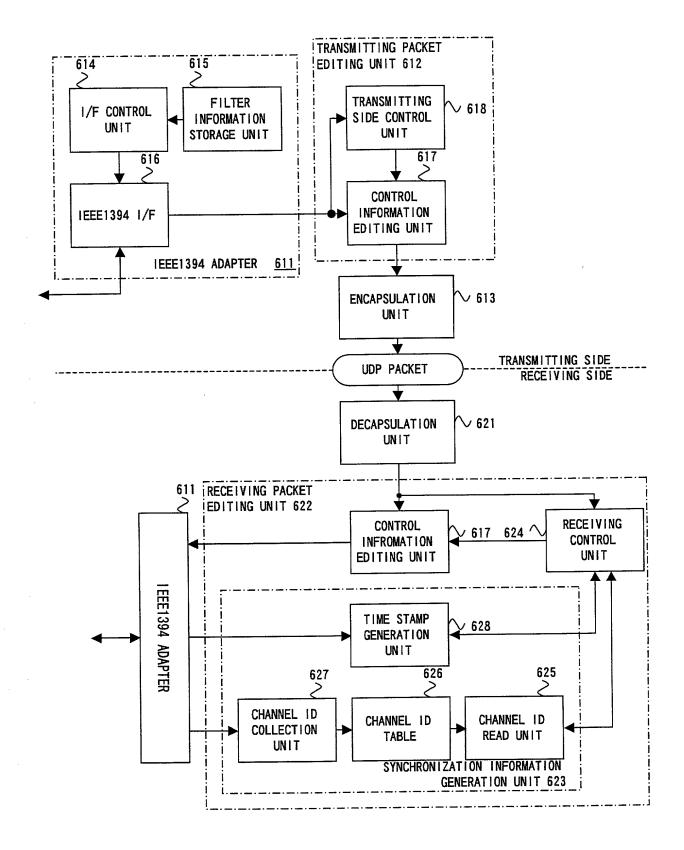


FIG. 32

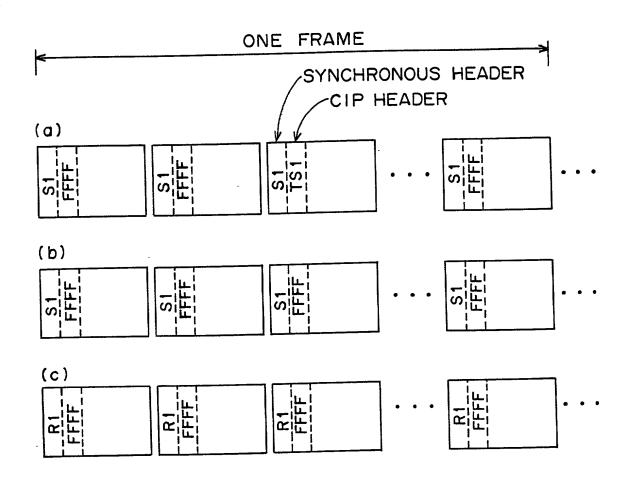


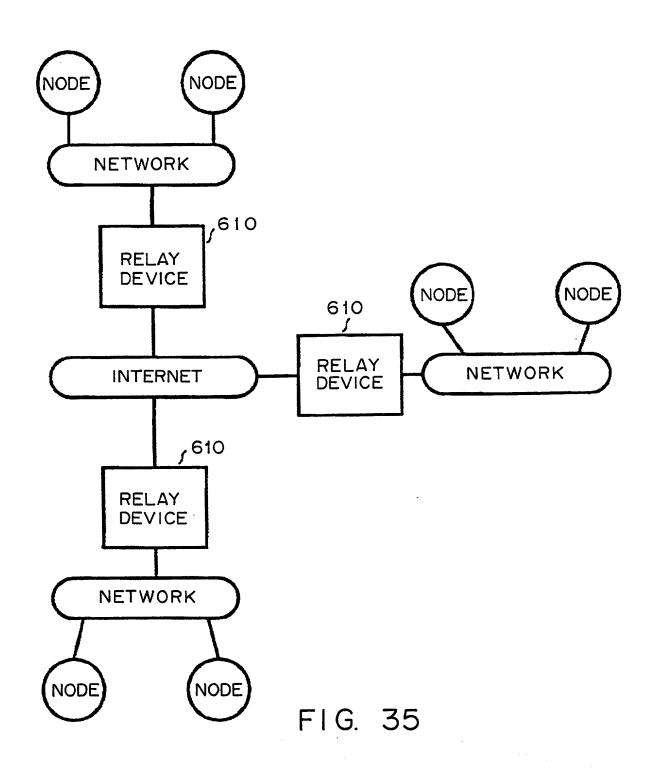
FIG. 33

TRANSMITTING SIDE CHANNEL ID	RECEIVING SIDE CHANNEL ID
S 1	R 1
•	•
•	•

FIG. 34A

TRANSMITTING SIDE CHANNEL ID	NETWORK ADDRESS	RECEIVING SIDE CHANNEL ID
S 1	N 1	R 1
S 1	N 2	R 2
S 2	N 1	R3
•	•	•
	•	•

FIG. 34B



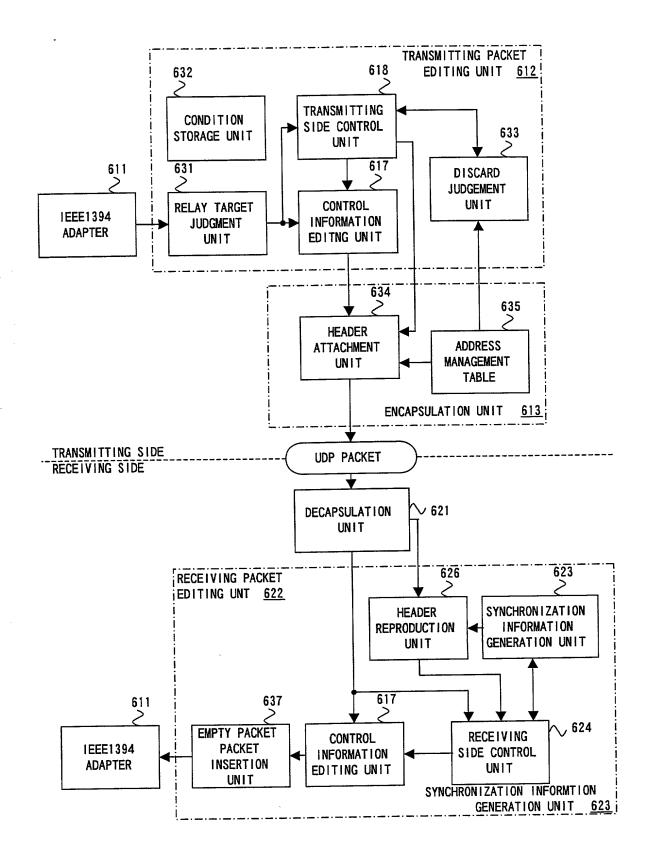
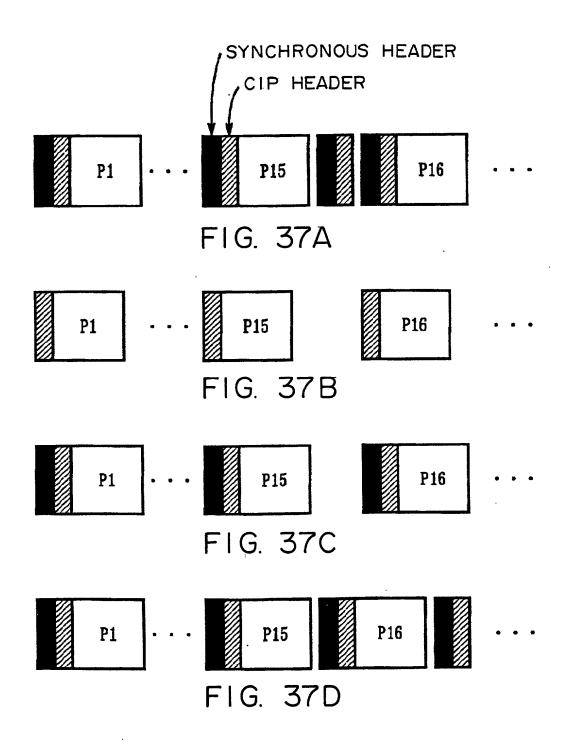


FIG. 36



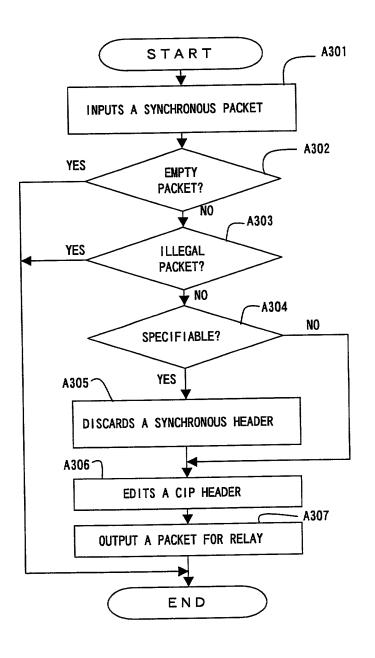


FIG. 38

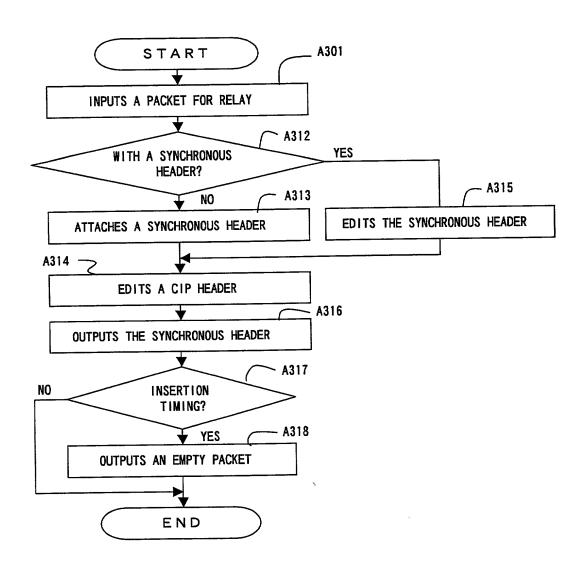


FIG. 39

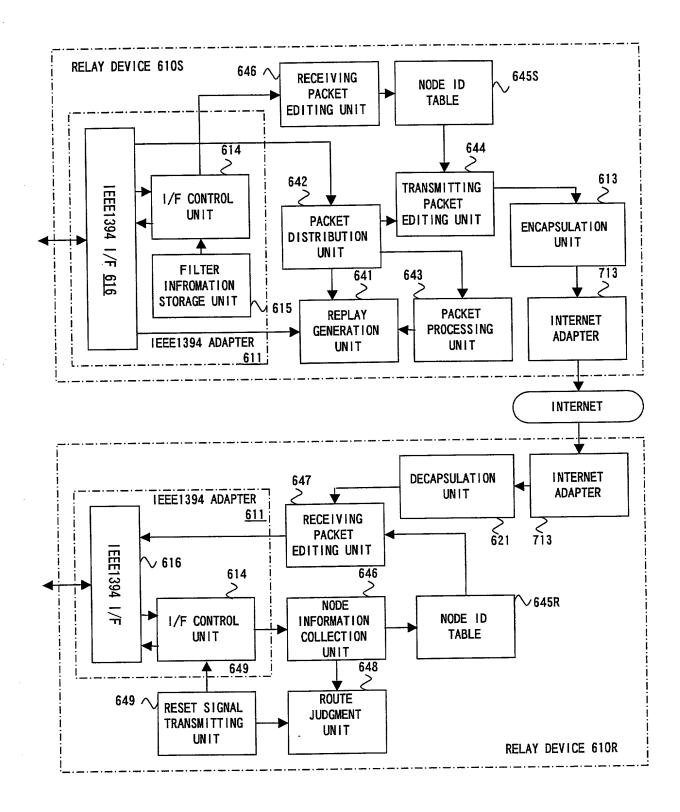


FIG. 40

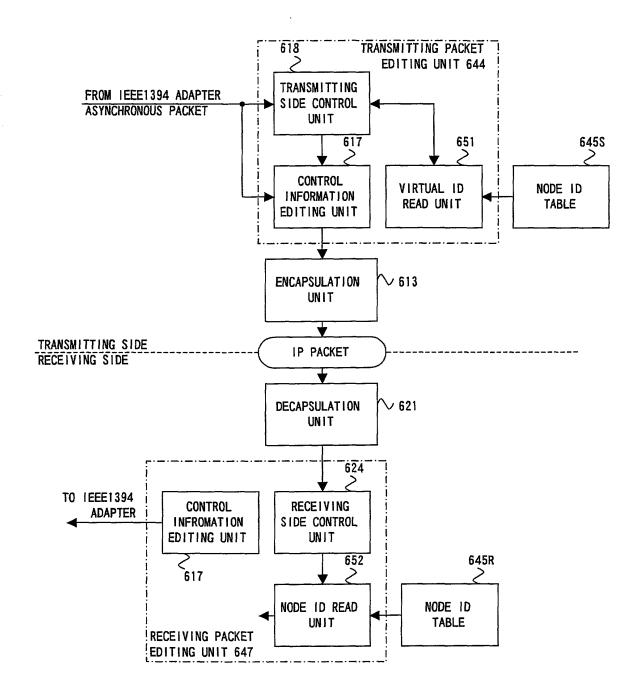


FIG. 41

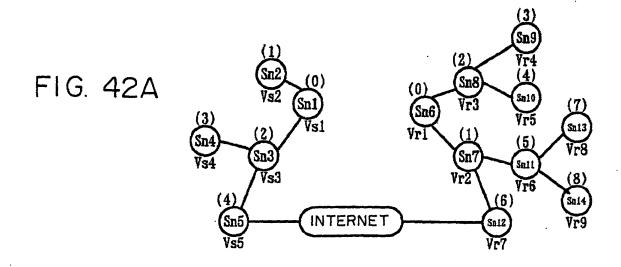
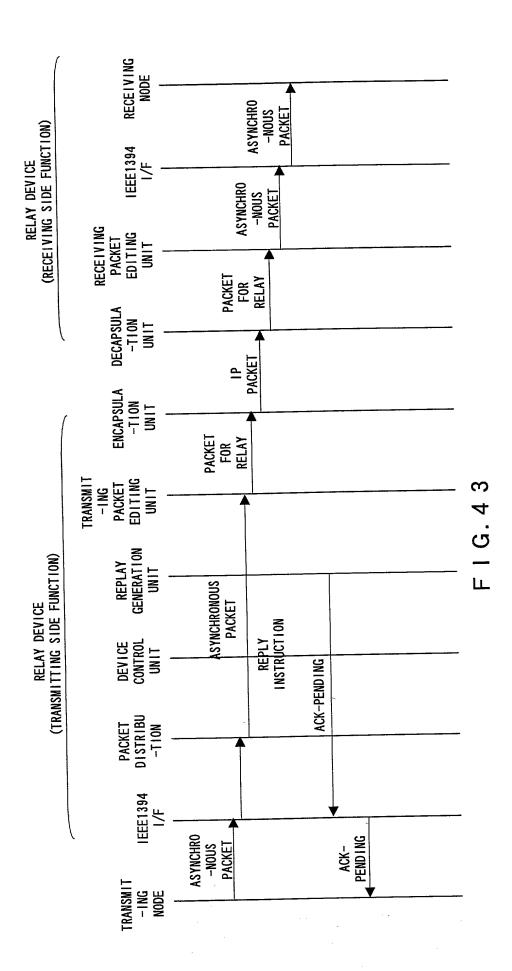


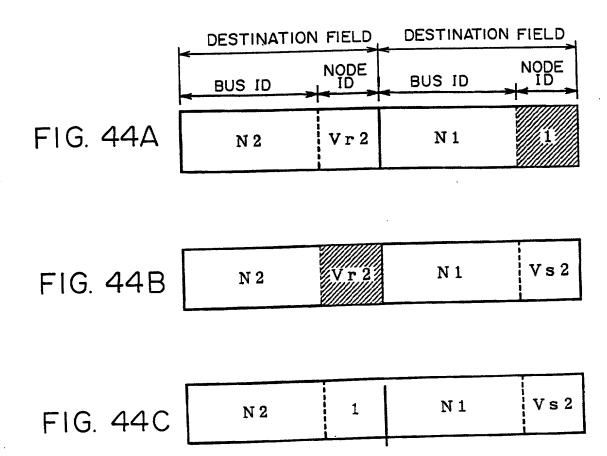
FIG. 42B

SERIAL No.	VIRTUAL ID	NODE ID
Sn1	Vs1	0
Sn2	Vs2	1
	•	•
•	•	•
•	•	•
Sn5	Vs5	4

FIG. 42C

SERIAL No.	VIRTUAL ID	NODE ID
Sn6	Vr1	0
Sn7	V r 2	1
•	•	•
	•	
S n 14	Vr9	8





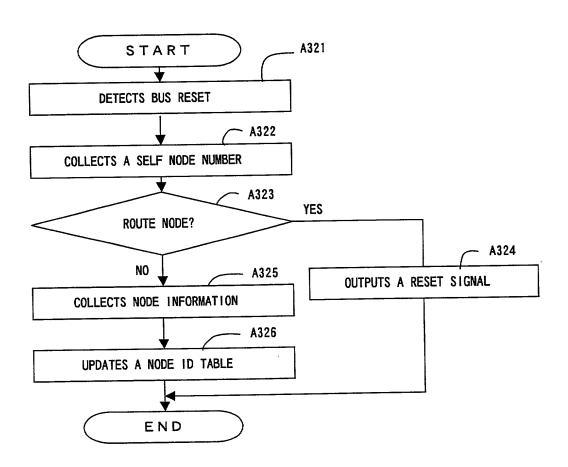


FIG. 45

RELAY DEVICE RESET NODE SIGNAL INFORMATION TRANSMITT ROUTE **COLLECTION IEEE1394** JUDGMENT -ING 1/F NODE1 NODE2 ··· NODE N UNIT UNIT UNIT RESET NOTIFICA -TION **SELF** ID COLLECTION SELF NODE ID RESET NOTIFICA -TION SELF ID SELF NODE COLLECTION ID SIRIAL NO. REQUEST SIRIAL NO. REQUEST COLLECT INSTRUCTION SIRIAL NO. REQUEST SIRIAL NO. REQUEST SIRIAL NO. REQUEST NODE INFORMATION SIRIAL NO. REQUEST

F I G. 46

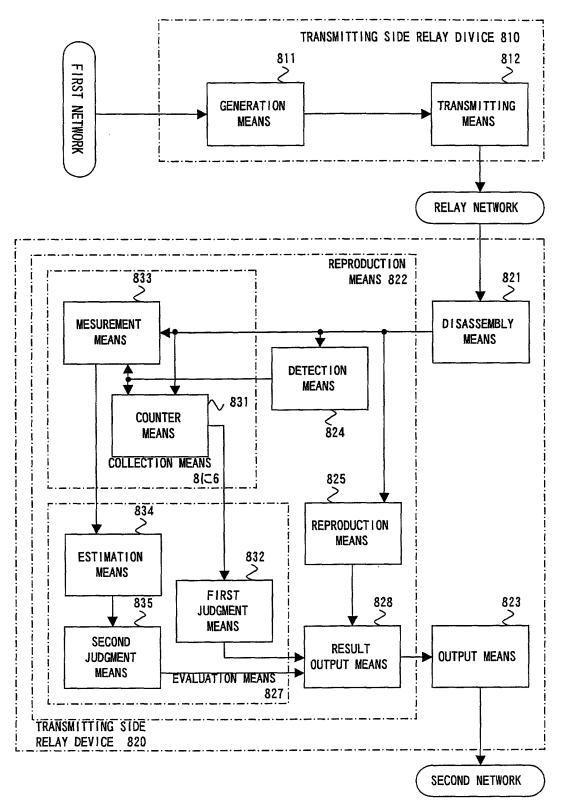


FIG. 47

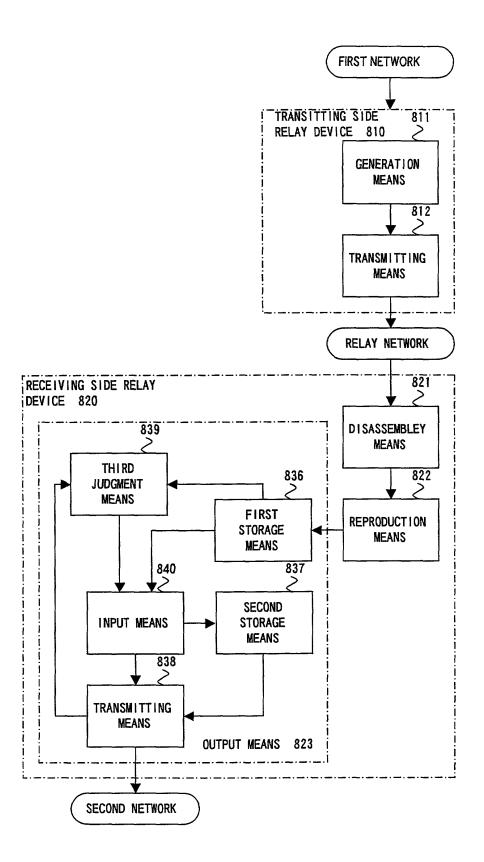


FIG. 48

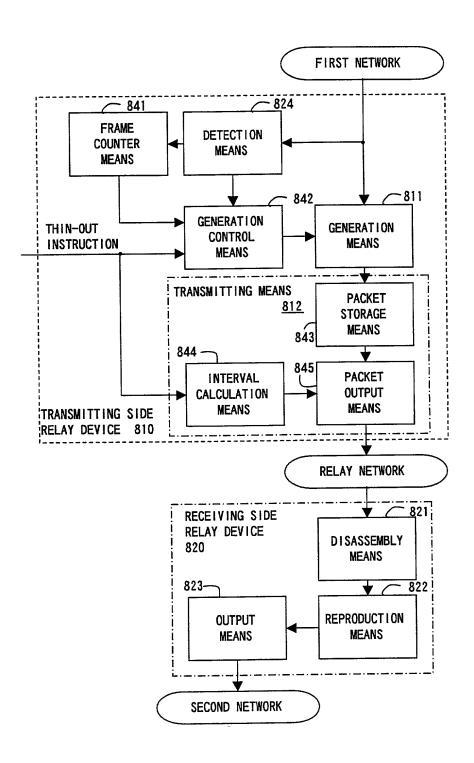


FIG. 49

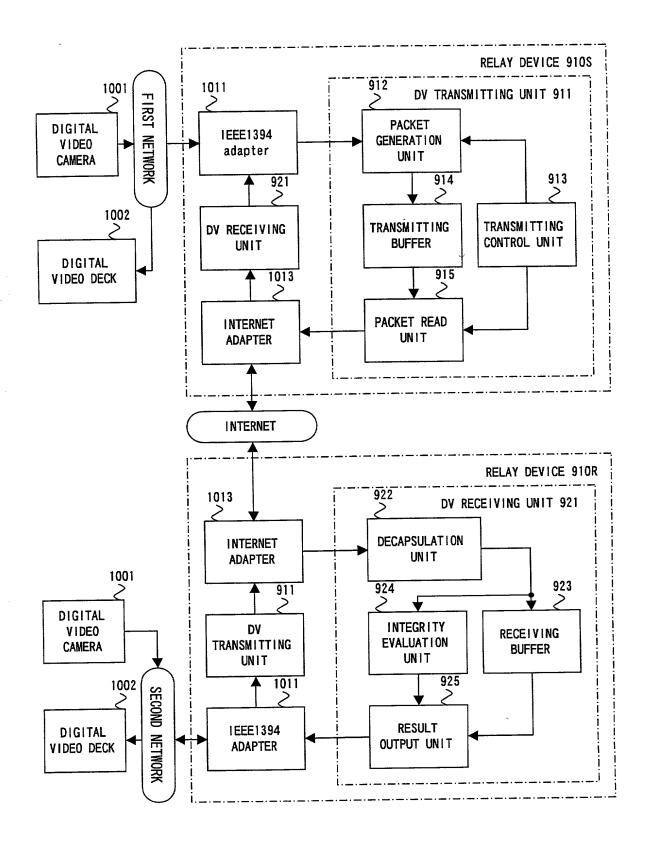


FIG. 50

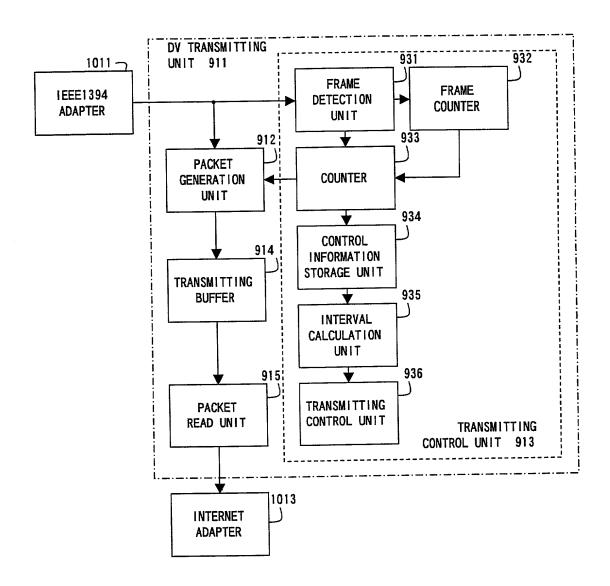


FIG. 51

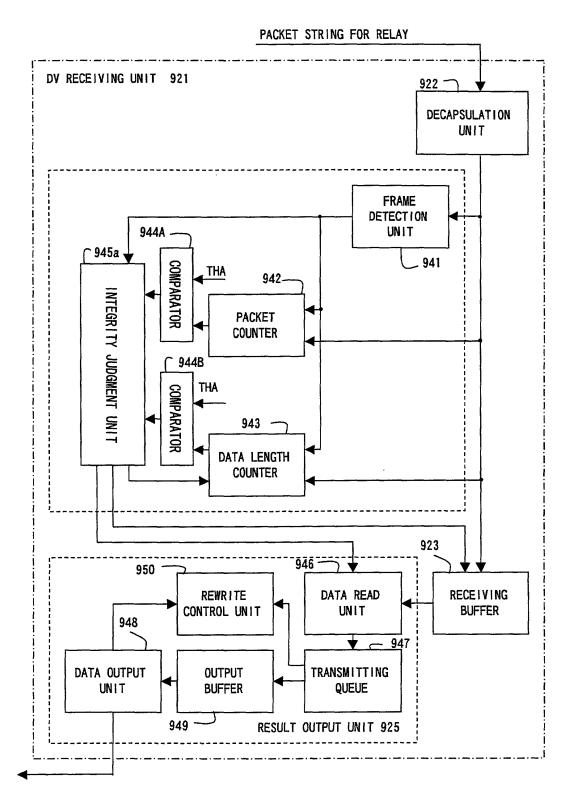


FIG. 52

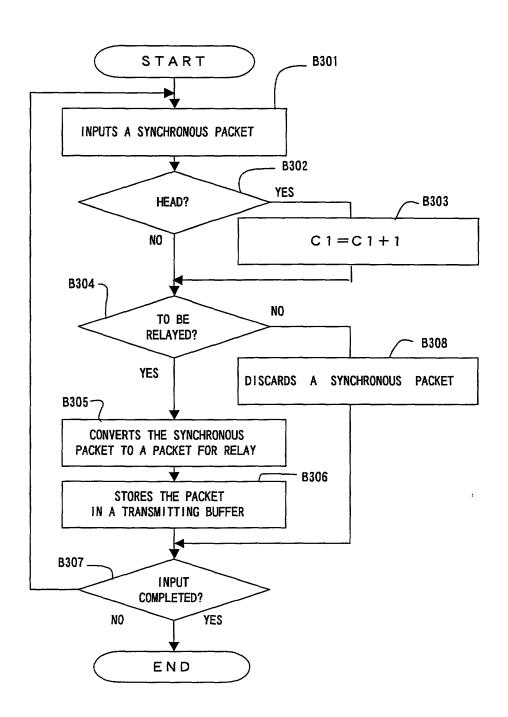


FIG. 53A

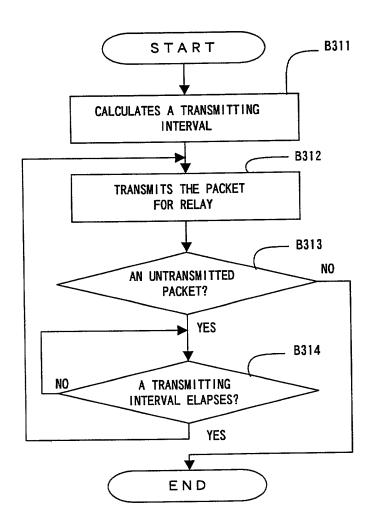
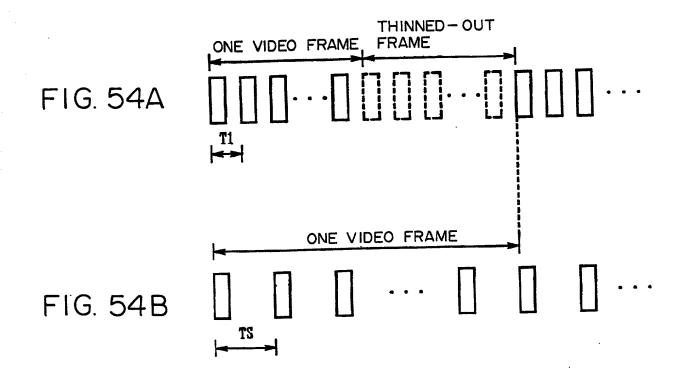


FIG. 53B



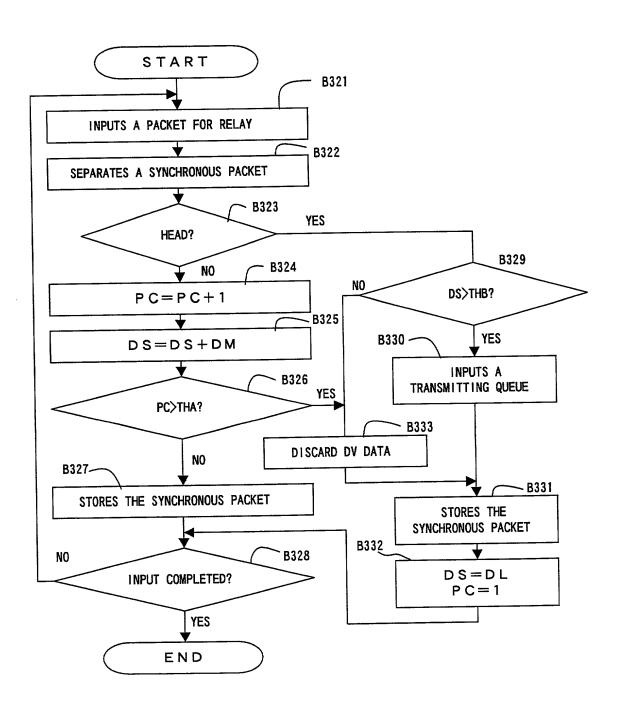
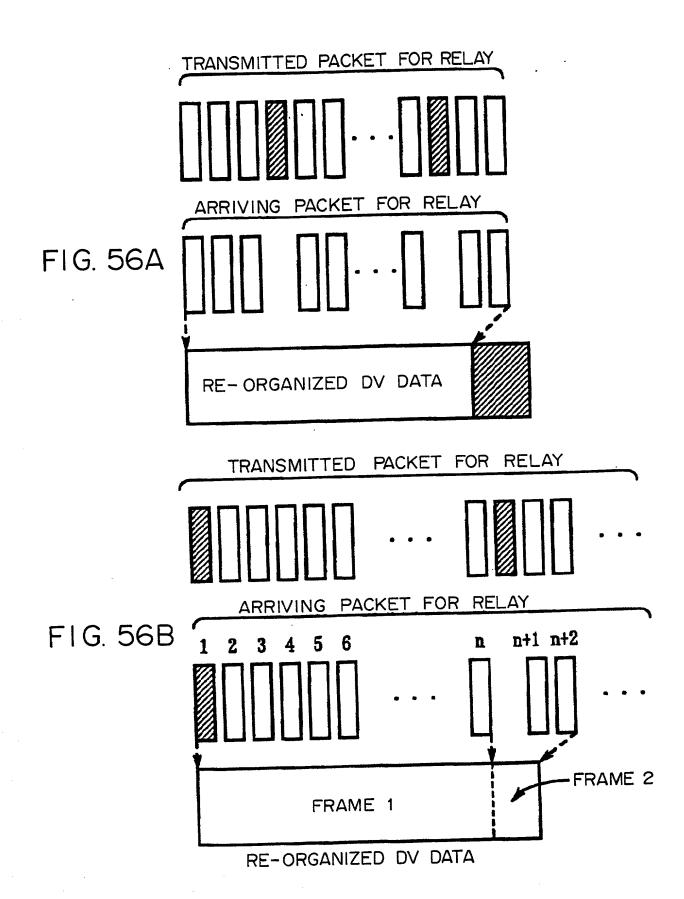


FIG. 55



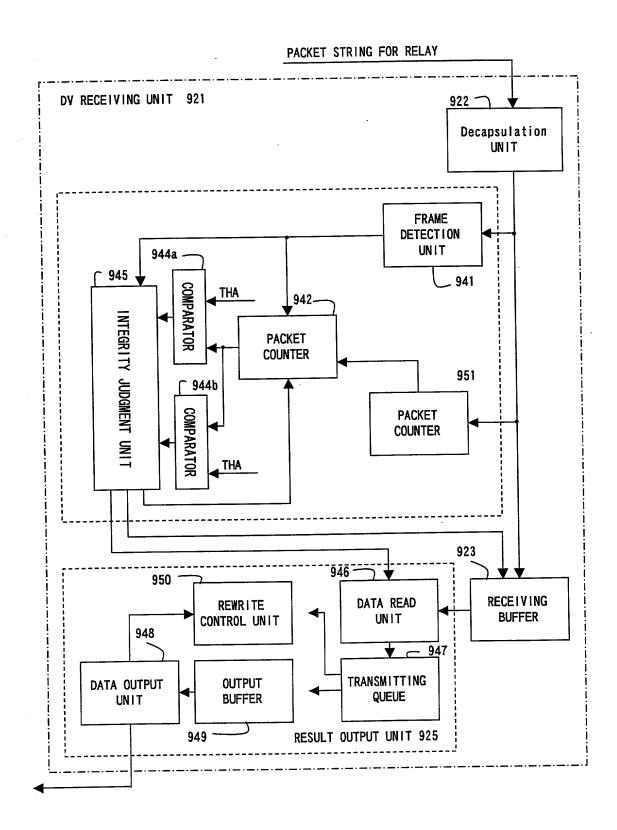


FIG. 57

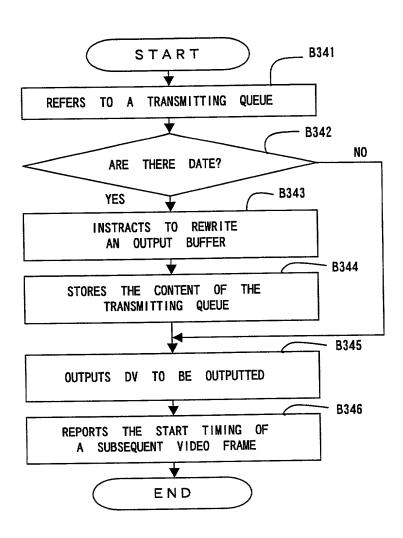
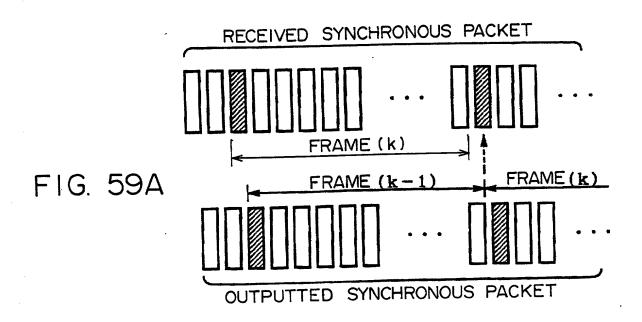
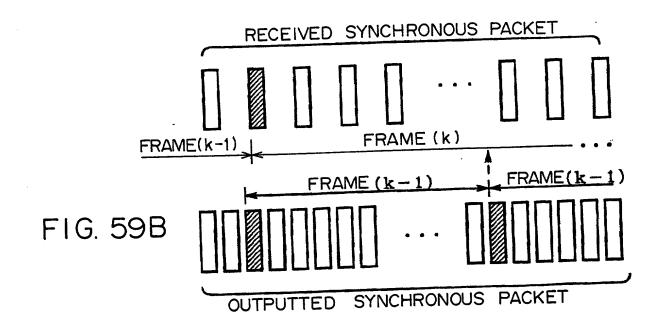
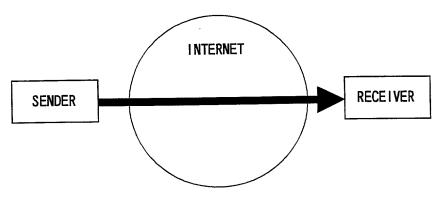


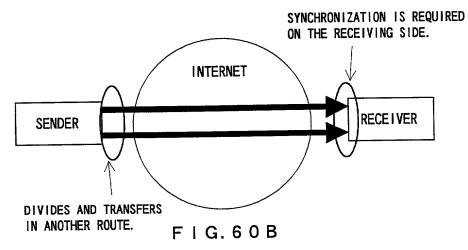
FIG. 58

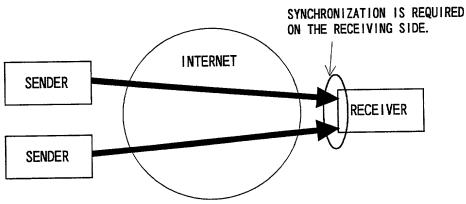




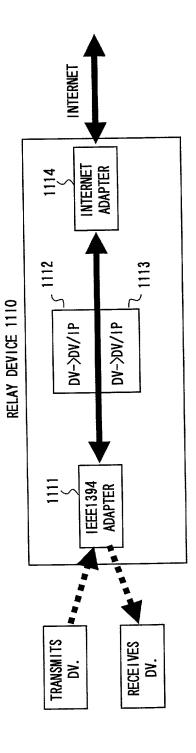


F I G. 6 0 A

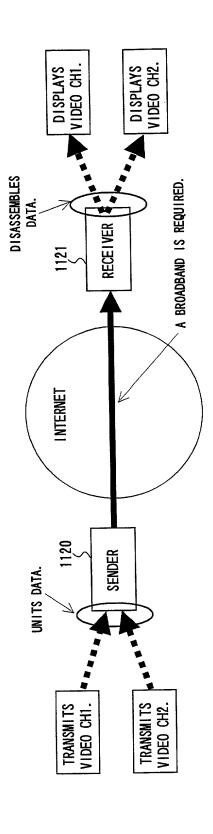




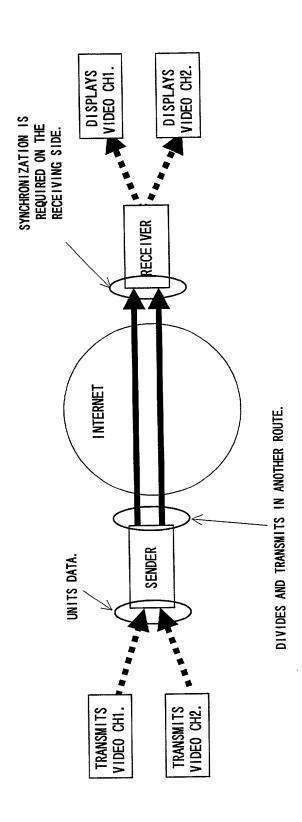
F | G. 60C



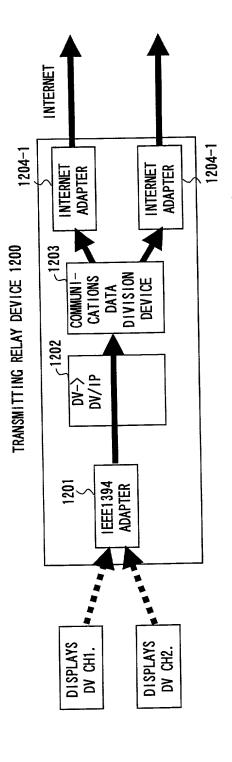
F I G. 6 1



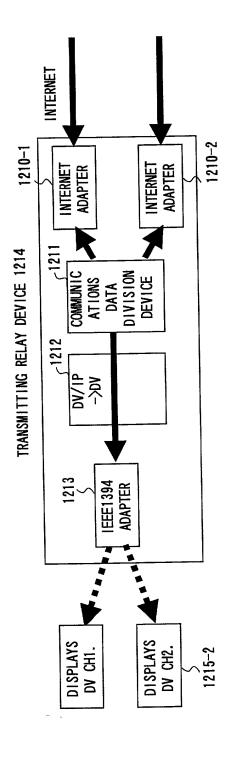
F I G. 62



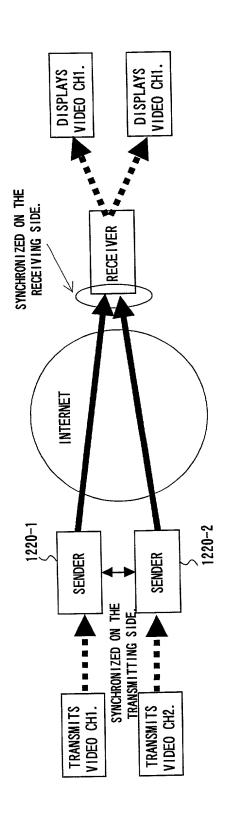
F I G. 63



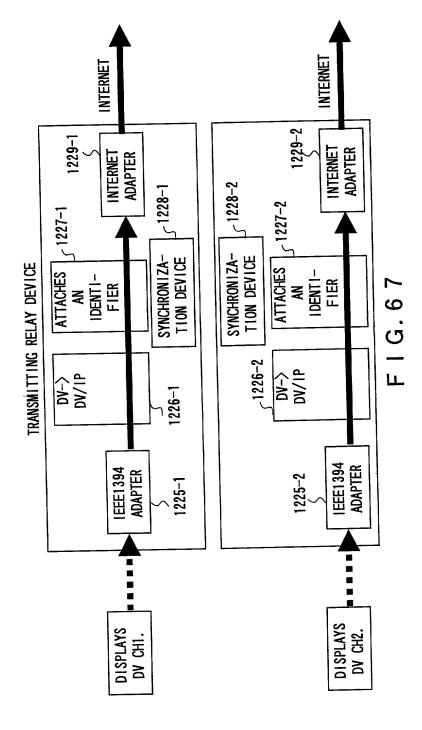
F I G. 64

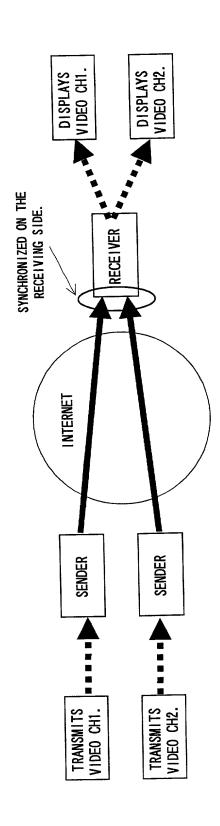


F I G. 65

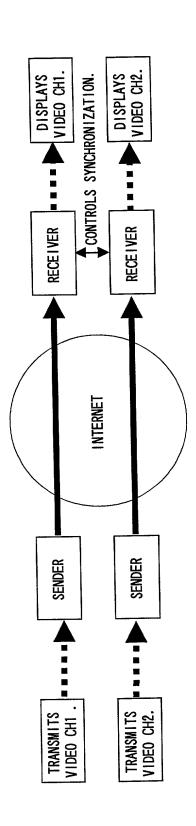


F I G. 66

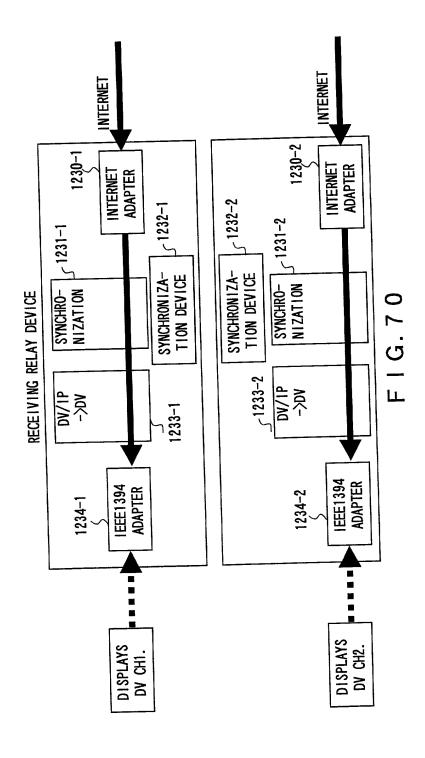


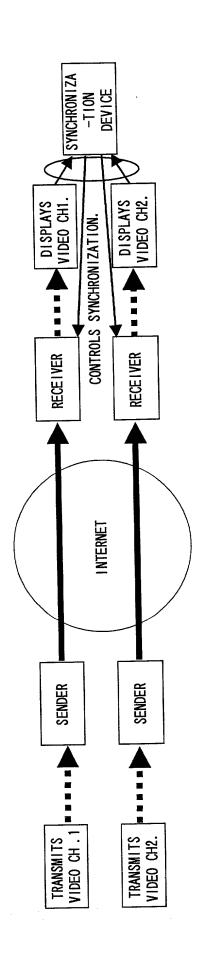


F I G. 68

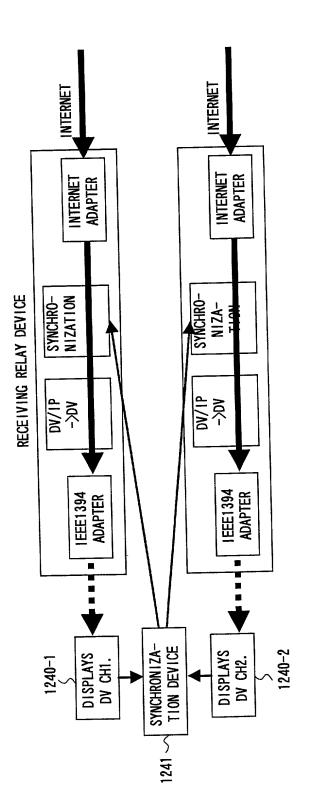


F I G. 69

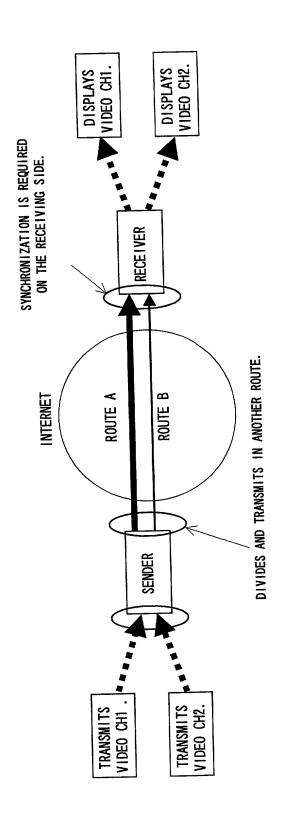




F I G. 71

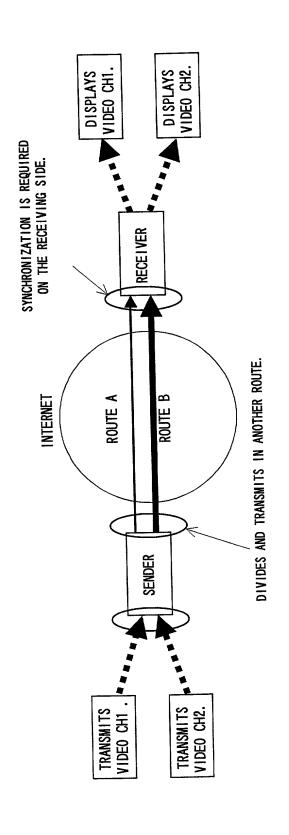


F I G. 72

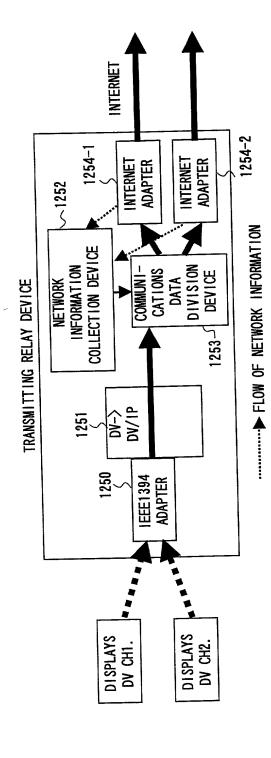


F I G. 73

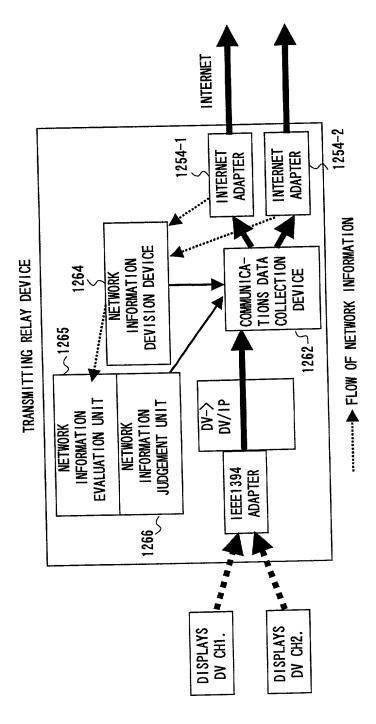
dh.



F I G. 74



F I G. 75



F I G. 76

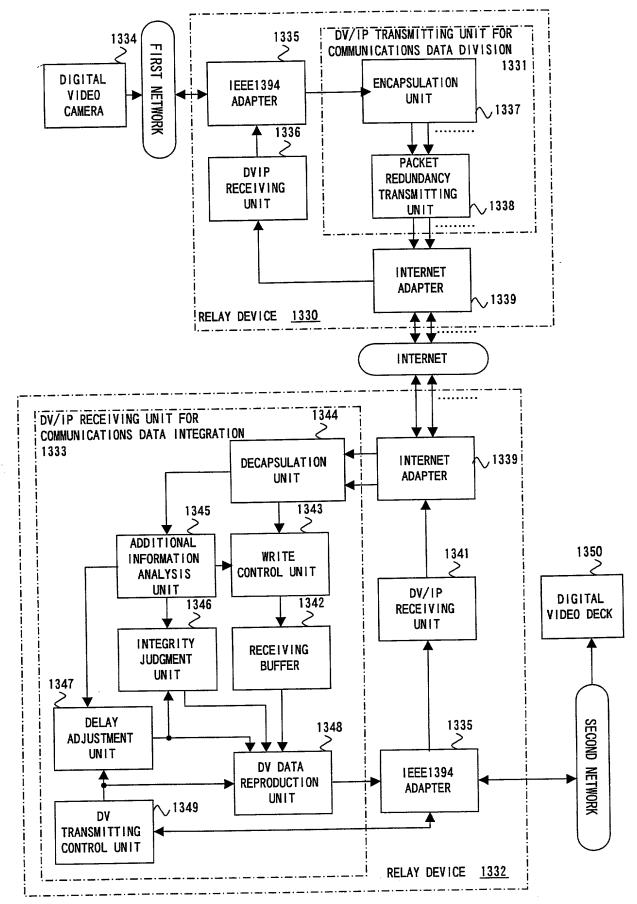
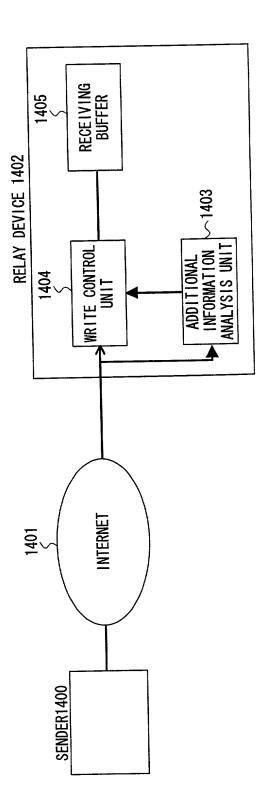


FIG. 77



F I G. 78